# Bridgelux WuXi R&D CO.,LTD

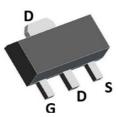
# 100V 8A N-Channel Enhancement Mode Power MOSFET

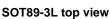
#### **FEATURES**

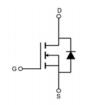
- RDSON $\leq$ 115m $\Omega$  @Vgs=10V, Id=3A
- Excellent RDS(ON) and Low Gate Charge
- · Lead free product is acquired

#### **APPLICATION**

- Power switching application
- · Hard switched and high frequency circuits
- Uninterruptible power supply







**Schematic Diagram** 

#### **ASSEMBLY MESSAGE**

Product Name	Marking	Package	Packaging	
BXT1150N10J	0103M	SOT89-3L	Reel	

#### ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Parameter		Symbol	Symbol Rating SOT89-3L		
Drain-Source Voltage		V <sub>DSS</sub>	100	>	
Drain Current	Con	tinuous (T <sub>C</sub> = 25°C)	I <sub>D</sub>	8	Α
Drain Current	Con	tinuous (T <sub>C</sub> = 100°C)	ID	5.6	Α
Drain Current	Pulsed (Note1)		I <sub>DM</sub>	32	Α
Gate-Source Voltage		V <sub>GSS</sub>	±20	<b>V</b>	
Power Dissipation T <sub>C</sub> =25°C		P <sub>D</sub>	2	W	
Maximum Junction Temperature		TJ	150	ů	
Storage Temperature Range		T <sub>STG</sub>	-55 to 150	°C	

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature



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## THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	l lmi4	
Farameter	Symbol	SOT89-3L	Unit	
Thermal Resistance, Junction to Ambient	Reja	62.5	°C/W	

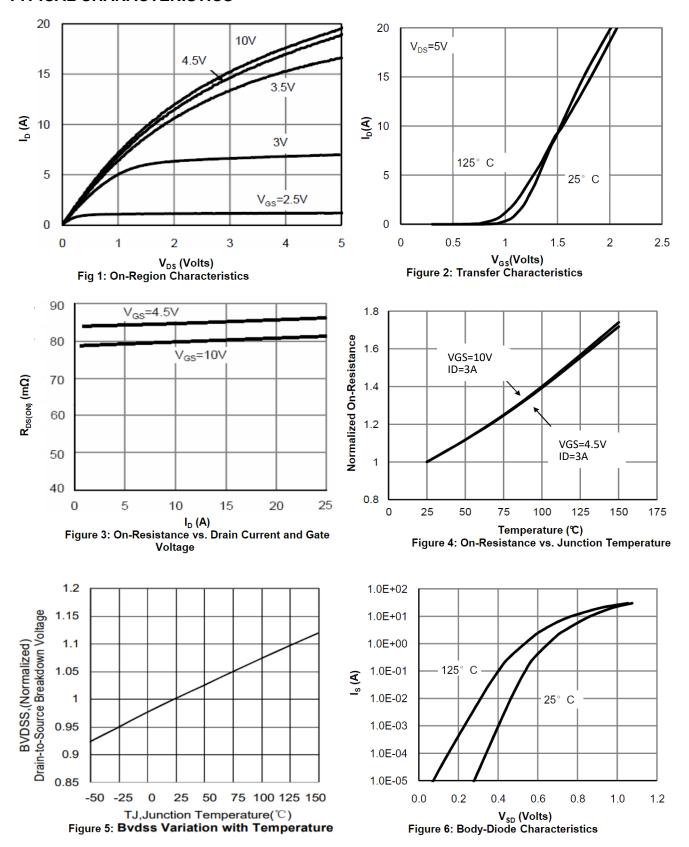
### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C,unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	VGS=0V, ID=250µA	100			V		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	VDS=100V, VGS=0V			1	uA		
Gate-Body Leakage Current, Forward	I <sub>GSS</sub>	VGS=20V			100	nA		
Gate-Body Leakage Current, Reverse		VGS=-20V			-100	nA		
ON CHARACTERISTICS								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	VDS=VGS, ID=250μA	1.0	2.0	3.0	V		
Drain Source On State Registence	D	VGS=10V, ID=3A		85	115	mΩ		
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	VGS=4.5V, ID=3A		110	135	mΩ		
DYNAMIC PARAMETERS								
Input Capacitance	Ciss	VDS=25V, VGS=0V, f=1.0MHz		655		pF		
Output Capacitance	Coss			25		pF		
Reverse Transfer Capacitance	Crss			21		pF		
SWITCHING PARAMETERS	SWITCHING PARAMETERS							
Turn-ON Delay Time	t <sub>D(ON)</sub>			6.2		ns		
Turn-ON Rise Time	t <sub>R</sub>	VDD=50V, ID=8A, VGS =		4.1		ns		
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	10V, RG=1.8Ω		21		ns		
Turn-OFF Fall-Time	t <sub>F</sub>			3.8		ns		
Total Gate Charge(Note2)	$Q_{G}$	VDQ 50V VQQ 40V 15		21		nC		
Gate Source Charge	Q <sub>GS</sub>	VDS =50V, VGS =10V, ID		2.2		nC		
Gate Drain Charge	$Q_{GD}$	=3A		3.3		nC		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	IS=8A, VGS=0V			1.4	V		
Diode Continuous Forward Current	Is				8	Α		
Maximum Pulsed Drain to Source Diode Forward Current	lsм				32	А		

Note: 2. Essentially independent of operating temperature



#### TYPICAL CHARACTERISTICS





# **TYPICAL CHARACTERISTICS(Cont.)**

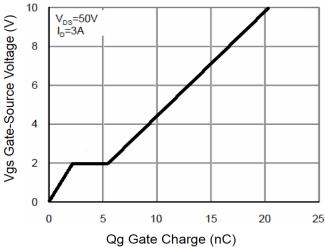
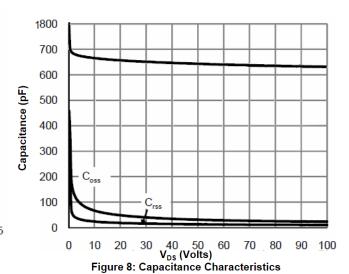


Figure 7: Gate-Charge Characteristics



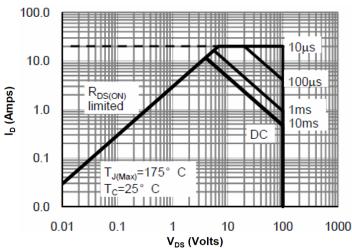
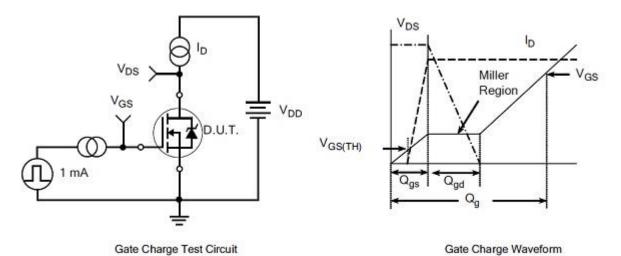
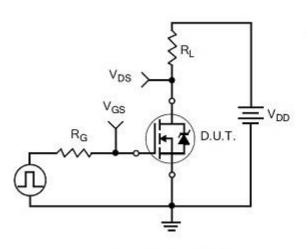


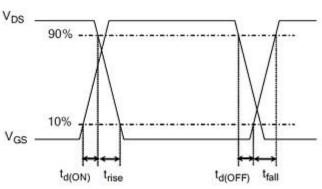
Figure 9: Maximum Forward Biased Safe Operating Area



#### **TEST CIRCUITS AND WAVEFORMS**





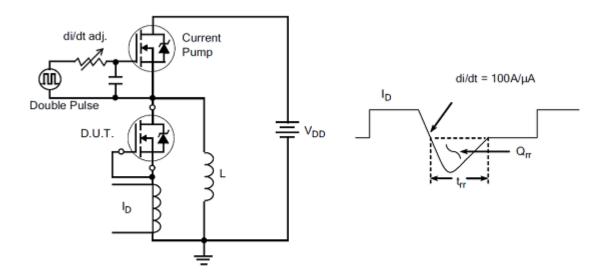


Resistive Switching Test Circuit

Resistive Switching Waveforms

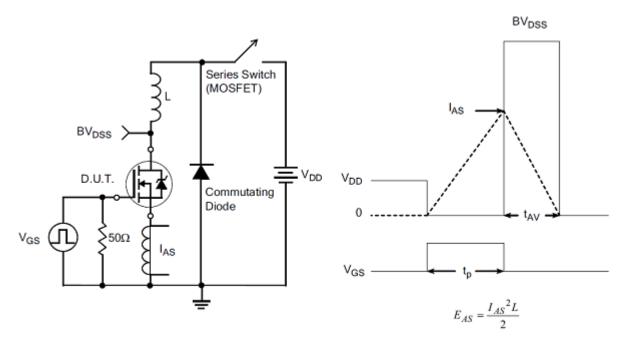
Version: 1.0

# **TEST CIRCUITS AND WAVEFORMS(Cont.)**



Diode Reverse Recovery Test Circuit

Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

Version: 1.0

# **Revision history**

# **Document revision history**

Date	Revision	Changes
2-Jul-2021	1.0	First release

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