

30V 30A N-Channel Enhancement Mode Power MOSFET

General Description

This Power MOSFET has been developed using advanced trench process, which is specifically designed to minimize input capacitance and gate charge. This renders the device suitable for use as primary switch in advanced high-efficiency isolated DC-DC converters for telecom and computer applications, and applications with low gate charge driving requirements.

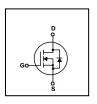
FEATURES

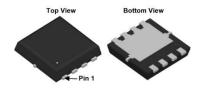
- RDSON \leq 8m Ω @Vgs=10V, Id=25A
- Excellent RDS(ON) and Low Gate Charge

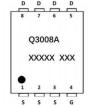
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· Lead free product is acquired

SYMBOL







PDFN3.3X3.3-8L

Marking and pin Assignment

ASSEMBLY MESSAGE

Product Name	Marking	Package	Packaging
BXT080N03E	Q3008A	PDFN3.3X3.3-8L	Reel

ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Parameter		Symbol	Rating	Unit	
		Cymbol	PDFN3.3X3.3-8L		
Drain-Source Voltage		V _{DSS}	30	V	
C C		inuous (T _C = 25°C)		30	А
Drain Current	Con	inuous (T _C = 100°C)	- I _D	20	А
Drain Current Pulsed (Note1)		I _{DM}	120	А	
Gate-Source Voltage		V _{GSS}	±20	V	
Power Dissipation T _C =25°C		P _D	12	W	
Maximum Junction Temperature		TJ	150	°C	
Storage Temperature Range		T _{STG}	-55 to 150	°C	

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature



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THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	Unit	
Parameter	Symbol	PDFN3.3X3.3-8L		
Thermal Resistance, Junction to Case	Rелс	10.4	°C/W	

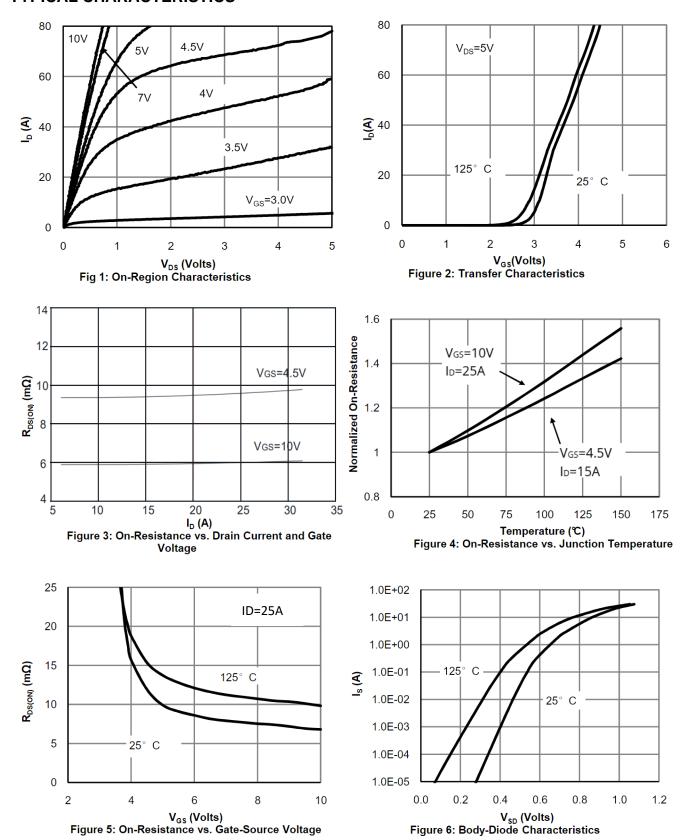
ELECTRICAL CHARACTERISTICS (T_J=25°C,unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS	-	1		JI.	l.	
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V, ID=250µA	30			V
Zero Gate Voltage Drain Current	IDSS	VDS=30V, VGS=0V			1	uA
Gate-Body Leakage Current, Forward		VGS=20V			100	nA
Gate-Body Leakage Current, Reverse	I _{GSS}	VGS=-20V			-100	nA
ON CHARACTERISTICS					•	
Gate Threshold Voltage	V _{GS(TH)}	VDS=VGS, ID=250µA	1.0	1.5	2.5	V
Dunin Course On State Besistance	_	VGS=10V, ID=25A		6	8	mΩ
Drain-Source On-State Resistance	R _{DS} (ON)	VGS=4.5V, ID=15A		9.5	14	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	Ciss	\/D0_45\/_\/00_6\/		608		pF
Output Capacitance	Coss	VDS=15V, VGS=0V, f=1.0MHz		272		pF
Reverse Transfer Capacitance	C _{RSS}			37		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}			16		ns
Turn-ON Rise Time	t _R	VDD=15V, ID=15A, VGS =		20		ns
Turn-OFF Delay Time	t _{D(OFF)}	10V, RG=3Ω		34		ns
Turn-OFF Fall-Time	t _F			22		ns
Total Gate Charge(Note2)	Q_{G}	VDC 45V VCC 40V ID		13.2		nC
Gate Source Charge	Q _{GS}	VDS =15V, VGS =10V, ID =15A		3.2		nC
Gate Drain Charge	Q _{GD}	=15A		5.1		nC
SOURCE- DRAIN DIODE RATINGS	AND CHARA	ACTERISTICS				
Drain-Source Diode Forward Voltage	V _{SD}	IS=30A, VGS=0V			1.2	V
Diode Continuous Forward Current	ls				30	Α
Maximum Pulsed Drain to Source	lova				120	٨
Diode Forward Current	lsм				120	Α
Body Diode Reverse Recovery Time	trr	IE-204 d1/d+-4004/		14		ns
Body Diode Reverse Recovery Charge	Qrr	IF=30A,dI/dt=100A/μs		4.1		nC

Note: 2. Essentially independent of operating temperature



TYPICAL CHARACTERISTICS





TYPICAL CHARACTERISTICS(Cont.)

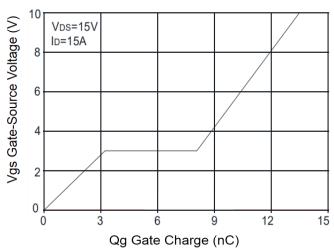


Figure 7: Gate-Charge Characteristics

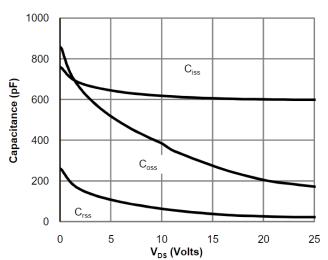


Figure 8: Capacitance Characteristics

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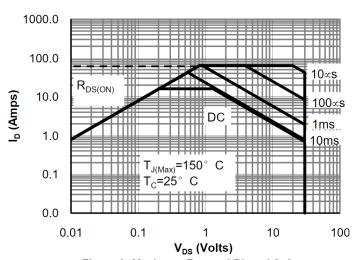
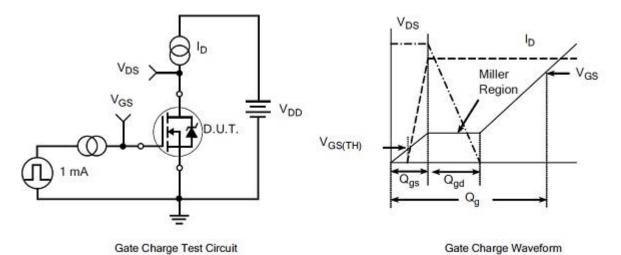
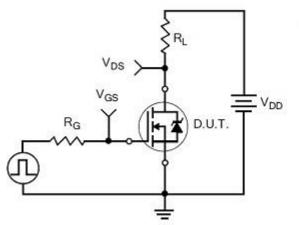


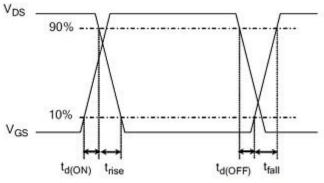
Figure 9: Maximum Forward Biased Safe Operating Area



TEST CIRCUITS AND WAVEFORMS





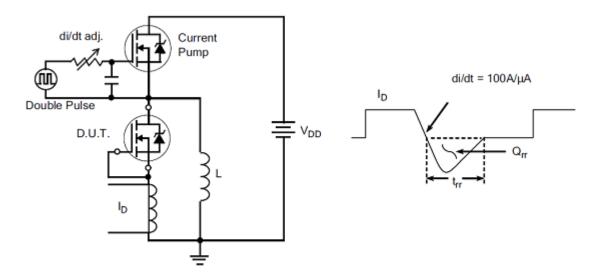


Resistive Switching Test Circuit

Resistive Switching Waveforms

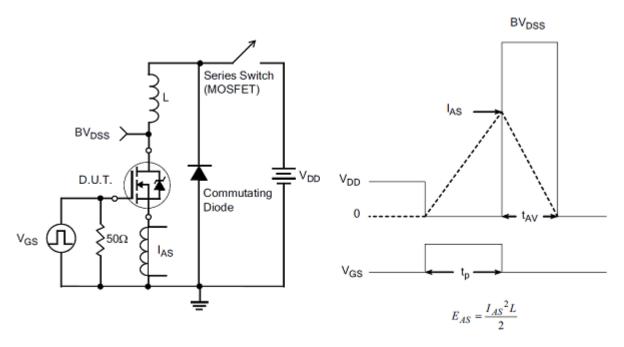


TEST CIRCUITS AND WAVEFORMS(Cont.)



Diode Reverse Recovery Test Circuit

Diode Reverse Recovery Waveform



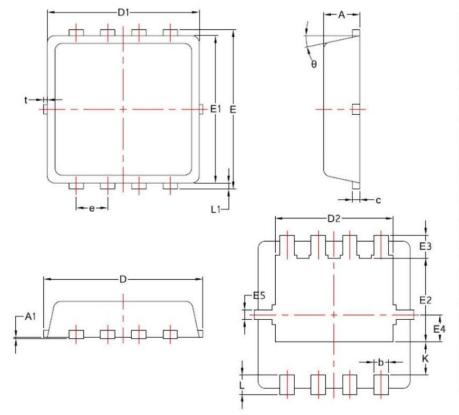
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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PDFN3.3X3.3-8L Package



S	COMMON				
M B O L	MM				
O	MIN	NOM	MAX		
Α	0.70	0.75	0.85		
A1	1	/	0.05		
b	0.20	0.30	0.40		
С	0.10	0.152	0.25		
D	3.15	3.30	3.45		
D1	3.00	3.15	3.25		
D2	2.29	2.45	2.65		
Е	3.15	3.30	3.45		
E1	2.90	3.05	3.20		
E2	1.54	1.74	1.94		
E3	0.28	0.48	0.65		
E4	0.37	0.57	0.77		
E5	0.10	0.20	0.30		
е	0.60	0.65	0.70		
K	0.59	0.69	0.89		
L	0.30	0.40	0.50		
L1	0.06	0.125	0.20		
t	0	0.075	0.13		
θ	10°	12°	14°		

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Revision history

Document revision history

Date	Revision	Changes
15-Mar-2021	1.0	First release

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